Title: IDENTIFYING NODES IN A RING NETWORK

IN THE SPECIFICATION

Please amend the specification as follows:

The paragraph beginning at page 5, line 6 is amended as follows:

As shown in Fig. 1, system 100 includes service processing switch 110, access routers 104, and network 116. In one embodiment, service processing switch 110 provides switching, routing, and computing resources that can be allocated by a service provider to customers. In one embodiment, service processing switch 110 is the IPSX 9000 service processing switch from CoSine Communications, Inc. But, the invention is not limited to any particular switch, router, or service processing hardware.

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The paragraph beginning at page , line 2 and 4 are amended as follows:

Fig. 2 is a block diagram of a multi-blade system connected via a ring network within service processing system 110, according to an embodiment of the invention. In some embodiments, each of two network rings 210 and 220 communicatively connect blades 112-1, 112-2, and 112-3 together. Although three blades are shown, in another embodiment any number can be present. Blade 112-1 contains processor 230-1 and memory 240-1 connected via system bus 250-1. Blade 112-1 also contains ring controller 145-1. Blade 112-2 contains processor 230-2 and memory 240-2 connected via system bus 250-2. Blade 112-1 also contains ring controller 145-2. Blade 112-3 contains processor 230-3 and memory 240-3 connected via system bus 250-3. Blade 112-1 also contains ring controller 145-3. Each blade optionally includes other hardware; for example although only one processor and memory are shown in the blades, each can contain multiple processors and multiple memories, as previously described above with reference to Fig. 1.

3. Please amend paragraphs [0028]-as follows:

[0028] FIG. 3B is a FIGS. 3B1 and 3B2 are cross-sectional [[view]] views taken along a line B-B in FIG. 3A;

4. Please amend paragraphs [0031]-as follows:

KM 1/12/07 [0030] FIG. 4B is a FIGS. 4B1 and 4B2 are cross-sectional [[view]] views taken along a line C-C in FIG. 4A; and

5. Please amend paragraph [0034] as follows:

[0034] As shown in FIG. 1B, [[the]] an earlier conventional wafer holder 2 includes a holder body 6, an opener 7, and a wafer side guard 8. The holder body 6 is substantially flat and circular. The opener 7 is formed by forming the portion overlapped with the wafer support 7 into a predetermined figure. The wafer side guard 8 is formed on a plate surface of the wafer holder 6 and is adhered to side edge of the semiconductor wafer 100 so that the process gas cannot penetrate through the side edge of the semiconductor wafer 100. The wafer side guard 8 is a ring that projects upwardly from the plate surface at the edge of the semiconductor wafer 100 as much as the thickness of the semiconductor wafer 100.

6. Please amend paragraph [0042] as follows:

[0042] FIG. 2A is a perspective view of a first embodiment of a wafer holder of the